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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/998,387	11/15/2001	Jerome Anthony Centanni JR.	AUS920010404US1	7330

35525 7590 07/02/2004

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EXAMINER

BONURA, TIMOTHY M

ART UNIT	PAPER NUMBER
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2114

DATE MAILED: 07/02/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/998,387

Applicant(s)

CENTANNI ET AL.

Examiner

Tim Bonura

Art Unit

2114

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 15 November 2001.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-36 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-36 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 12 February 2002 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☒ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>4</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-36 are rejected under 35 U.S.C. 102(b) as being anticipated by Carter, U.S.

Patent Number 5,963,722.

3. Regarding claim 1:

a. Regarding the limitation of “reading a plurality of trace vectors from a file on storage device,” Carter discloses a system wherein data is tracked, stored and used for resource monitoring of data from bus objects from the system. (Lines 15-20 of Column 3).

b. Regarding the limitation of “identifying a subset of the trace vectors, wherein the subset of the trace vectors forms a packet,” Carter discloses a system wherein each cycle list can be read from the bus during bus cycles as the system polls data from bus state machine objects. (Lines 29-3 of Column 7).

c. Regarding the limitation of “identifying a plurality of data fields within the packet,” Carter discloses a system wherein various state and transaction information can be identified from the information. (Lines 65-67 of Column 3 and Lines 1-2 of Column 4).

Art Unit: 2114

- d. Regarding the limitation of "presenting each of the data fields as output," Carter discloses a system wherein the cycle state machine enters a finish state as an indication that all the bits of data have finished processing and have been accounted for. (Lines 60-65 of Column 9).
4. Regarding claim 2, Carter discloses a system wherein there is a multiplicity of bus cycle lists generated. Each list contains various information. (Lines 48-50 and Lines 65-67 of Column 2).
5. Regarding claim 3, Carter discloses a system that contains a second subset of lists. (Lines 48-50 of Column 2).
6. Regarding claim 4, Carter discloses a system with storage objects (Lines 32-36 of Column 6).
7. Regarding claim 5, Carter discloses a system with storage objects, which would include either optical disk or magnetic disks. (Lines 32-36 of Column 6).
8. Regarding claim 6, Carter discloses a system wherein the storage objects contain information within the computer system. (Lines 32-36 of Column 6).
9. Regarding claim 7, Carter discloses a system wherein bus cycle lists are read from machine objects. (Lines 50-54 of Column 8). Carter also discloses a system wherein the lists are stored for use in determining errors. (Lines 58-67 of Column 8).
10. Regarding claim 8, Carter discloses the system uses logic analyzers. (Line 54 of Column 6).
11. Regarding claim 9, Carter discloses the system uses logic analyzers that are connected to the system. (Line 54 of Column 6).

Art Unit: 2114

12. Regarding claim 10, Carter discloses a system which can operates with a clock cycle.

(Lines 15-20 of Column 9).

13. Regarding claim 11, Carter discloses a system which can operates on the change of clock cycles. (Lines 60-65 of Column 9).

14. Regarding claim 12, Carter discloses a system with a flag bit. (Lines 10-14 of Column 9).

15. Regarding claim 13:

e. Regarding the limitation of “reading a plurality of trace vectors from a file on storage device,” Carter discloses a system wherein data is tracked, stored and used for resource monitoring of data from bus objects from the system. (Lines 15-20 of Column 3).

f. Regarding the limitation of “identifying a subset of the trace vectors, wherein the subset of the trace vectors forms a packet,” Carter discloses a system wherein each cycle list can be read from the bus during bus cycles as the system polls data from bus state machine objects. (Lines 29-3 of Column 7).

g. Regarding the limitation of “identifying a plurality of data fields within the packet,” Carter discloses a system wherein various state and transaction information can be identified from the information. (Lines 65-67 of Column 3 and Lines 1-2 of Column 4).

h. Regarding the limitation of “presenting each of the data fields as output,” Carter discloses a system wherein the cycle state machine enters a finish state as in indication

Art Unit: 2114

that all the bits of data have finished processing and have been accounted for. (Lines 60-65 of Column 9).

16. Regarding claim 14, Carter discloses a system wherein there is a multiplicity of bus cycle lists generated. Each list contains various information. (Lines 48-50 and Lines 65-67 of Column 2).

17. Regarding claim 15, Carter discloses a system that contains a second subset of lists. (Lines 48-50 of Column 2).

18. Regarding claim 16, Carter discloses a system with storage objects (Lines 32-36 of Column 6).

19. Regarding claim 17, Carter discloses a system with storage objects, which would include either optical disk or magnetic disks. (Lines 32-36 of Column 6).

20. Regarding claim 18, Carter discloses a system wherein the storage objects contain information within the computer system. (Lines 32-36 of Column 6).

21. Regarding claim 19, Carter discloses a system wherein bus cycle lists are read from machine objects. (Lines 50-54 of Column 8). Carter also discloses a system wherein the lists are stored for uses in determining errors. (Lines 58-67 of Column 8).

22. Regarding claim 20, Carter discloses the system uses logic analyzers. (Line 54 of Column 6).

23. Regarding claim 21, Carter discloses the system uses logic analyzers that are connected to the system. (Line 54 of Column 6).

24. Regarding claim 22, Carter discloses a system which can operates with a clock cycle. (Lines 15-20 of Column 9).

Art Unit: 2114

25. Regarding claim 23, Carter discloses a system which can operates on the change of clock cycles. (Lines 60-65 of Column 9).

26. Regarding claim 24, Carter discloses a system with a flag bit. (Lines 10-14 of Column 9).

27. Regarding claim 25:

i. Regarding the limitation of "a bus system," Carter discloses a system with a bus. (Line 39 of Column 4).

j. Regarding the limitation of "a processing unit connected to the bus system and including at least one processor," Carter discloses a system with a CPU. (Line 40 of Column 4).

k. Regarding the limitation of "memory connected to the bus system," Carter discloses a system with cache, DRAM, and PCI devices. (Line 41 of Column 4).

l. Regarding the limitation of "a set of instructions stored in the memory," Carter discloses a system with a bus bridge verification program written in a program language of implemented on hardware. Either of the options would require storage capacity. (Lines 46-54 of Column 4).

m. Regarding the limitation of "reading a plurality of trace vectors from a file on storage device," Carter discloses a system wherein data is tracked, stored and used for resource monitoring of data from bus objects from the system. (Lines 15-20 of Column 3).

n. Regarding the limitation of "identifying a subset of the trace vectors, wherein the subset of the trace vectors forms a packet," Carter discloses a system wherein each cycle

Art Unit: 2114

list can be read from the bus during bus cycles as the system polls data from bus state machine objects. (Lines 29-3 of Column 7).

o. Regarding the limitation of “identifying a plurality of data fields within the packet,” Carter discloses a system wherein various state and transaction information can be identified from the information. (Lines 65-67 of Column 3 and Lines 1-2 of Column 4).

p. Regarding the limitation of “presenting each of the data fields as output,” Carter discloses a system wherein the cycle state machine enters a finish state as in indication that all the bits of data have finished processing and have been accounted for. (Lines 60-65 of Column 9).

28. Regarding claim 26, Carter discloses a system wherein there is a multiplicity of bus cycle lists generated. Each list contains various information. (Lines 48-50 and Lines 65-67 of Column 2).

29. Regarding claim 27, Carter discloses a system that contains a second subset of lists. (Lines 48-50 of Column 2).

30. Regarding claim 28, Carter discloses a system with storage objects (Lines 32-36 of Column 6).

31. Regarding claim 29, Carter discloses a system with storage objects, which would include either optical disk or magnetic disks. (Lines 32-36 of Column 6).

32. Regarding claim 30, Carter discloses a system wherein the storage objects contain information within the computer system. (Lines 32-36 of Column 6).

Art Unit: 2114

33. Regarding claim 31, Carter discloses a system wherein bus cycle lists are read from machine objects. (Lines 50-54 of Column 8). Carter also discloses a system wherein the lists are stored for uses in determining errors. (Lines 58-67 of Column 8).

34. Regarding claim 32, Carter discloses the system uses logic analyzers. (Line 54 of Column 6).

35. Regarding claim 33, Carter discloses the system uses logic analyzers that are connected to the system. (Line 54 of Column 6).

36. Regarding claim 34, Carter discloses a system which can operates with a clock cycle. (Lines 15-20 of Column 9).

37. Regarding claim 35, Carter discloses a system which can operates on the change of clock cycles. (Lines 60-65 of Column 9).

38. Regarding claim 36, Carter discloses a system with a flag bit. (Lines 10-14 of Column 9).

Conclusion

39. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **Tim Bonura**.

- o The examiner can normally be reached on **Mon-Fri: 7:30-5:00, every other Friday off**. The examiner can be reached at: **703-305-7762**.

40. If attempts to reach the examiner by telephone are unsuccessful, please contact the examiner's supervisor, **Rob Beausoliel**.

- o The supervisor can be reached on **703-305-9713**.

Art Unit: 2114

41. The fax phone numbers for the organization where this application or proceeding is assigned are:

- **703-872-9306 for all patent related correspondence by FAX.**

42. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov/>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

43. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the **receptionist** whose telephone number is: **703-305-3900**.

44. Responses should be mailed to:


- **Commissioner of Patents and Trademarks**

P.O. Box 1450

Alexandria, VA 22313-1450

Tim Bonura
Examiner
Art Unit 2114

tmb
June 28, 2004



SCOTT BADERMAN
PRIMARY EXAMINER